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(71) Applicant: AT&T Corp.
New York, NY 10013-2412 (US)

(72) Inventors
• Lee, Kuo-Hua
Orlando, Florida 32836 (US)

• Yu, Chen-Hua Douglas
Orlando, Florida 32836 (US)

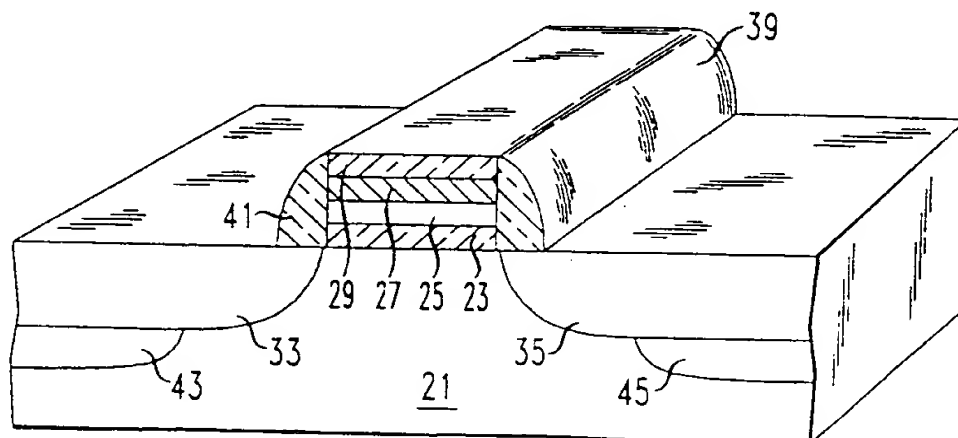
(74) Representative
Watts, Christopher Malcolm Kelway, Dr. et al
Lucent Technologies (UK) Ltd,
5 Mornington Road
Woodford Green Essex, IG8 0TU (GB)

(54) Dual gate formation

(57) A structure and method for fabrication of an integrated circuit having adjoining n^+ and p^+ gates (25 31) is disclosed. The adjoining n^+ and p^+ gates are covered by a titanium nitride layer (27). The titanium nitride layer is covered by a protective layer (29), illustratively made from PETEOS and by flanking spacers (not shown in

FIG 2) The protective layers help to prevent oxidation of the titanium nitride layer. (Oxidation of the titanium nitride layer would destroy its shunting capability.) Also disclosed is a heating step which simultaneously activates source/drain implants as well as flowing an overlying dielectric

FIG. 4



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EUROPEAN SEARCH REPORT

Application Number
EP 95 30 8754

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	PATENT ABSTRACTS OF JAPAN vol. 11, no. 292 (E-543) 19 September 1987 & JP-A-62 092 360 (TOSHIBA) 27 April 1987 * the whole document *	1-11	H01L29/423 H01L21/28
A	US-A-4 890 141 (T.E. TANG ET AL.) * column 2; figure 8 *	1-11	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H01L
The present search report has been drawn up for all claims.			
Place of search BERLIN		Date of completion of the search 20 March 1996	Examiner JUHL, A
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

EPF FORM 150 (3.87) (P/C/01)



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CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

- ☐ All claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claims
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

☒ LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims 1-11:
Gate structure consisting of p and n doped polysilicon, conductive layer and a patterned dielectric layer and method therefor.
2. Claim 12,13:
Method for forming source and drain regions in substrate.

☐ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.

☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respects of which search fees have been paid

namely claims

☒ None of the further search fees has been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims

namely claims 1-11

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APPLICANT: Thomas Hanel, del

LEARNER AND GREENBERG P.A.

P.O. BOX 2480

HOLLYWOOD, FLORIDA 33022

TEL. (954) 925-1100